

FIG. 1

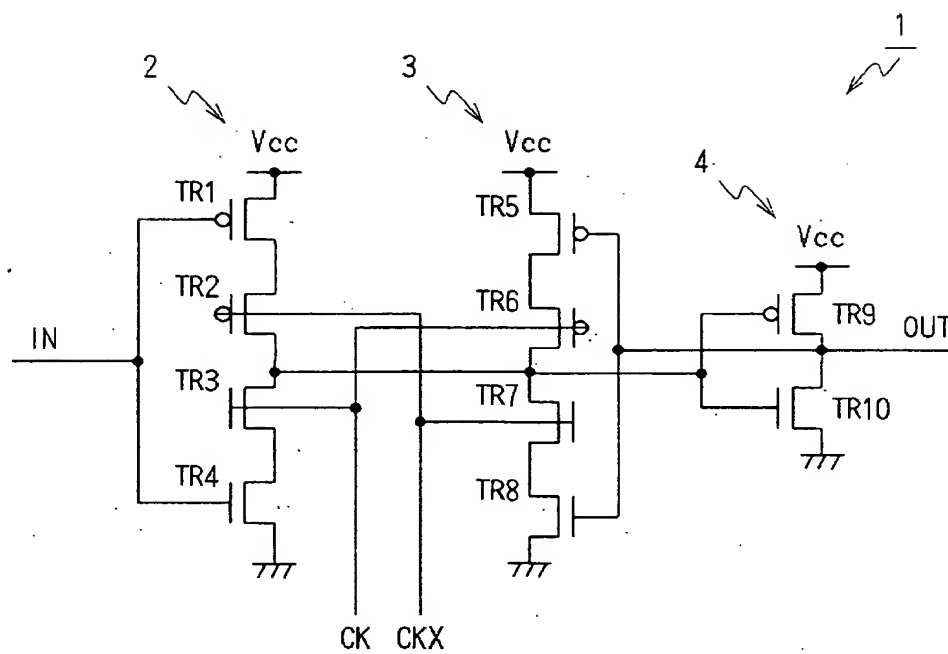


FIG. 2

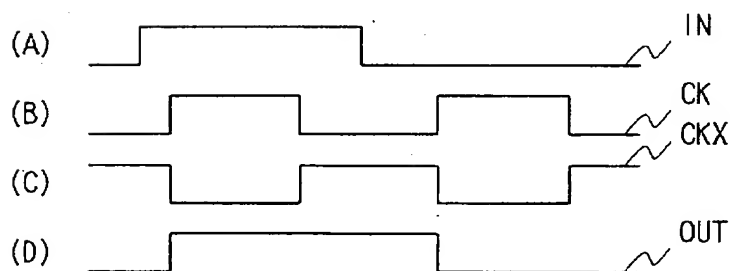


FIG. 3

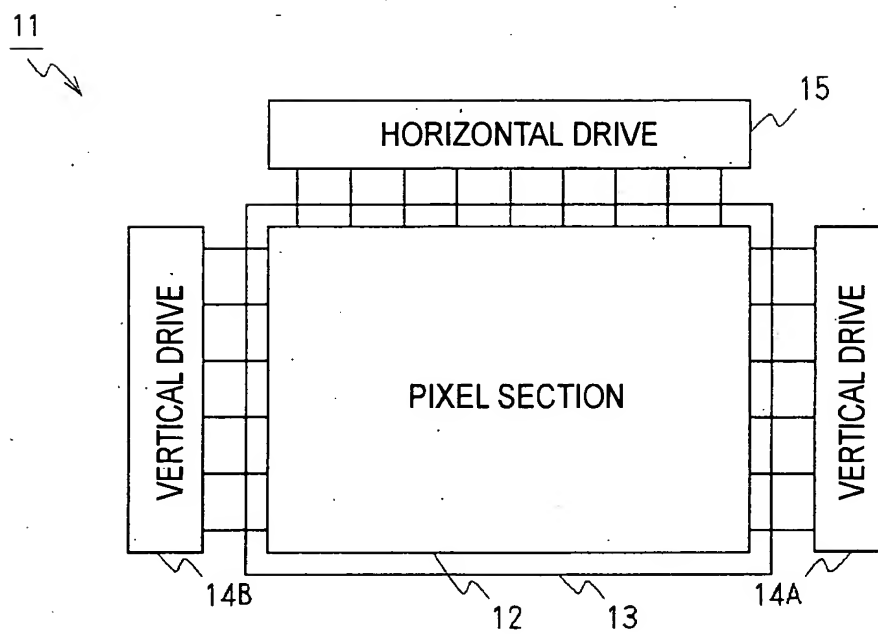


FIG. 4

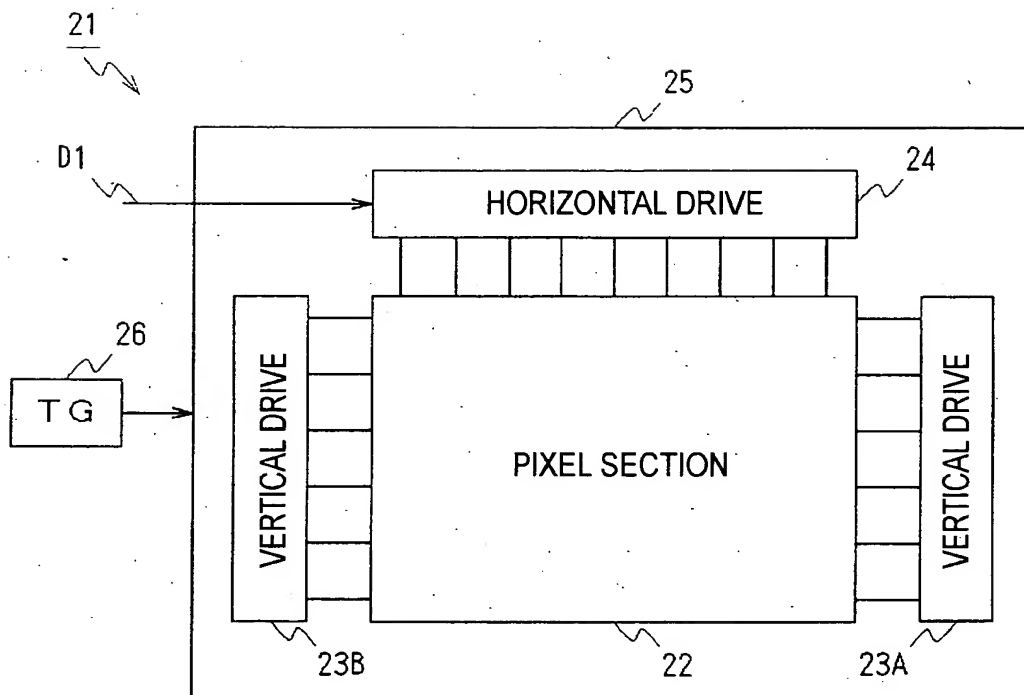
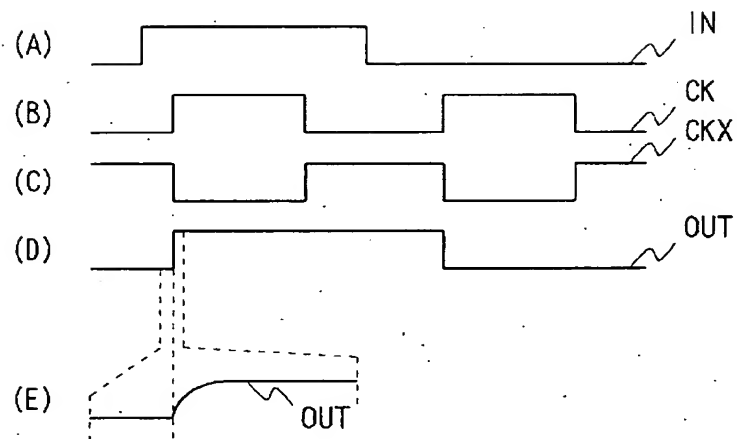


FIG. 6



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FIG. 7

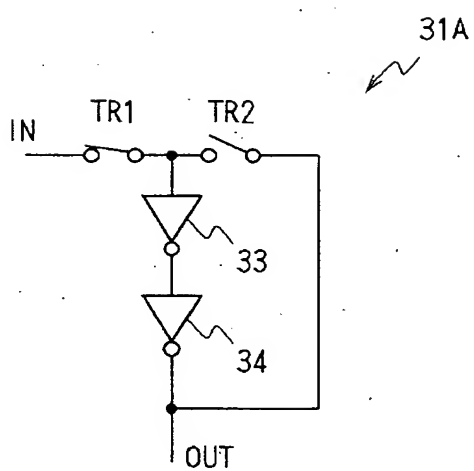
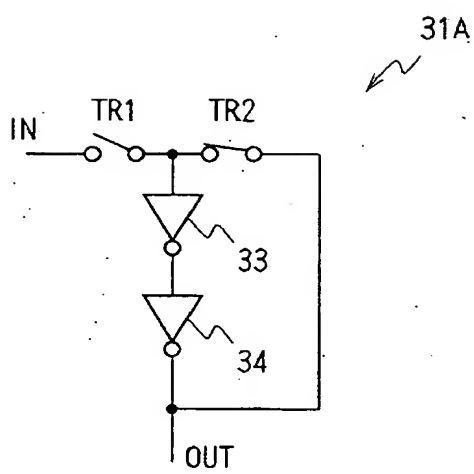
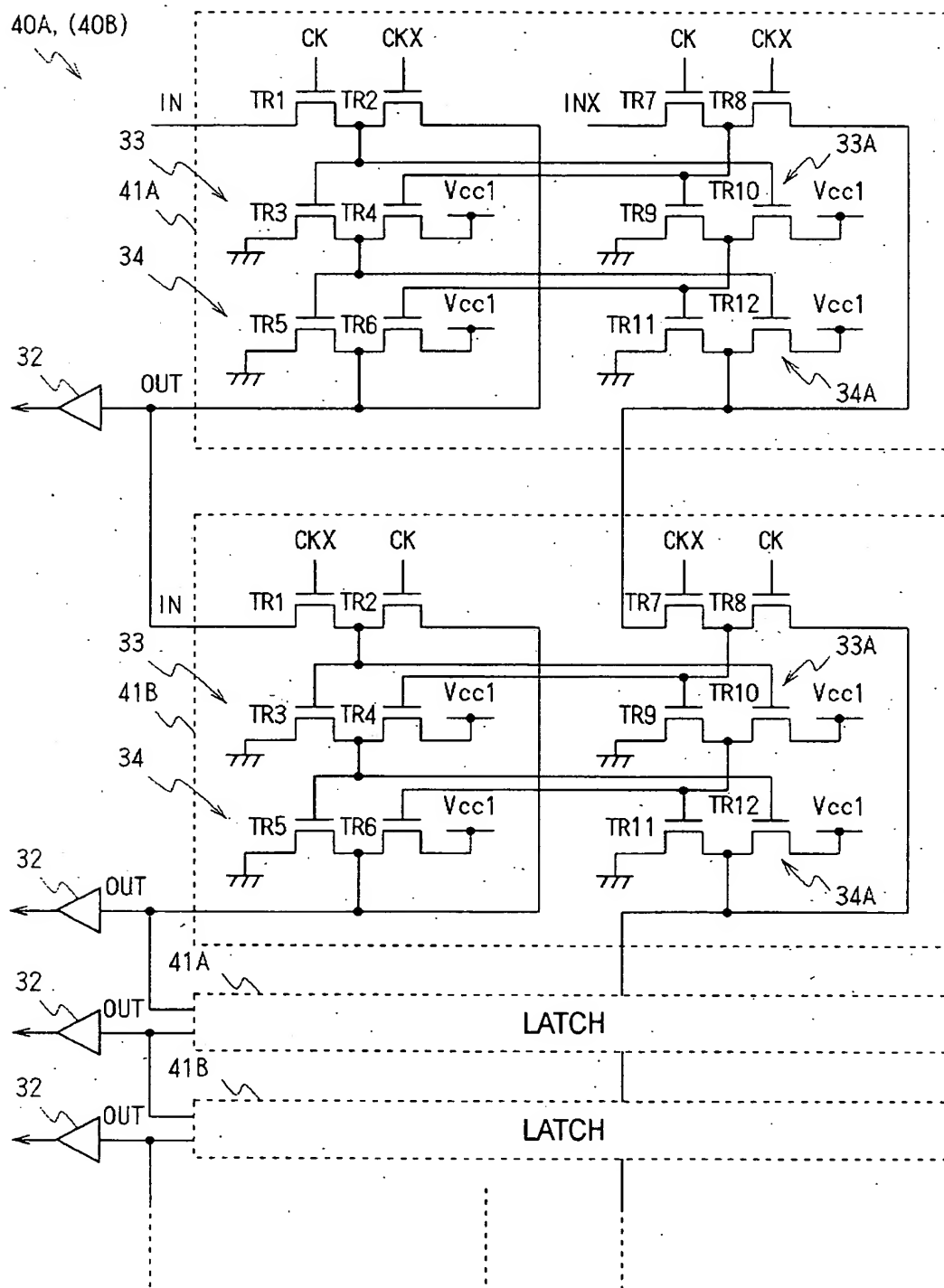


FIG. 8



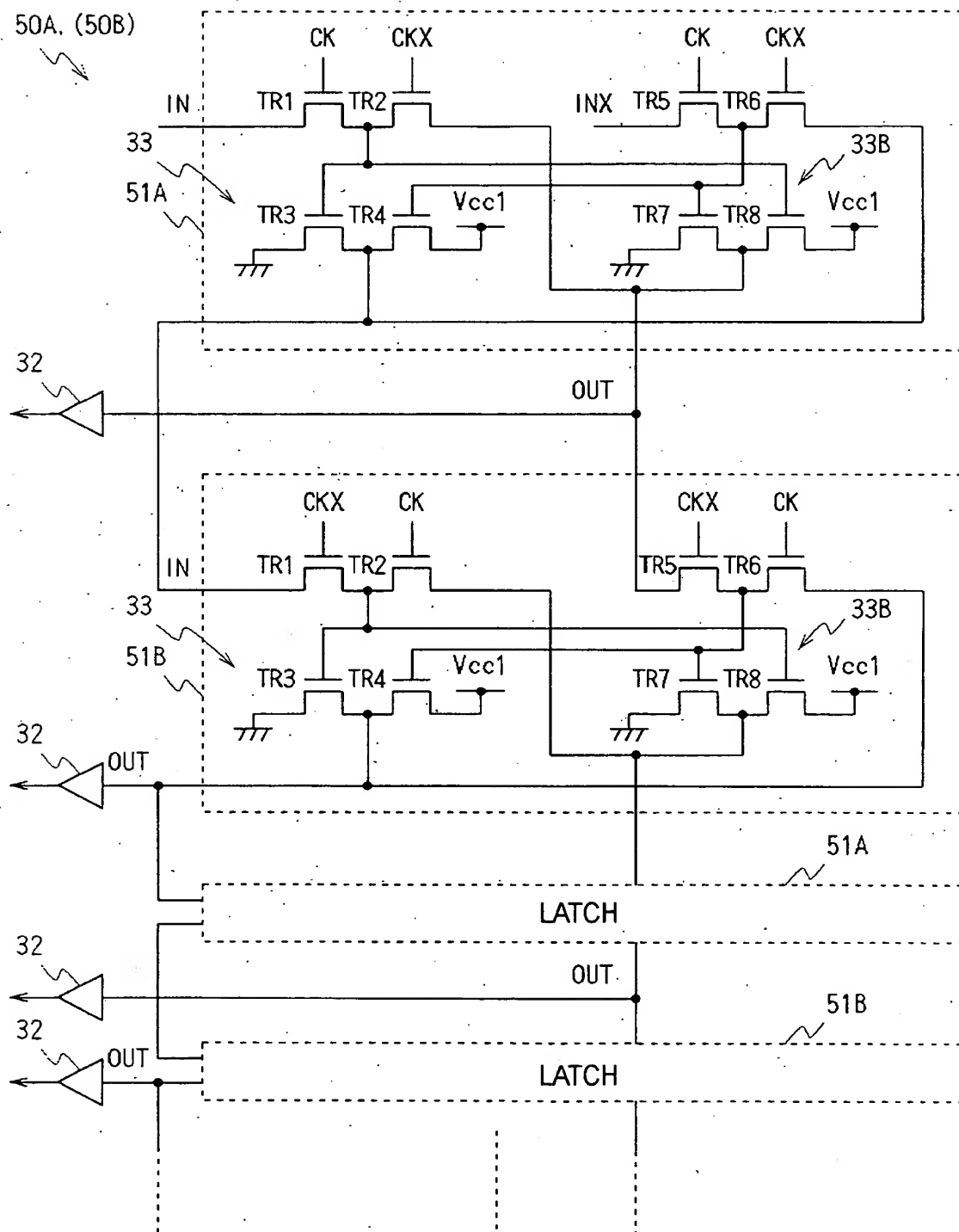
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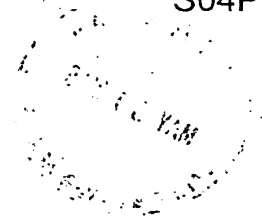
FIG. 9



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FIG. 10





DESCRIPTION OF REFERENCE CHARACTER

1, 31A, 31B, 41A, 41B, 51A, 51B LATCH CIRCUIT, 2, 3
CLOCKED INVERTER CIRCUIT, 4, 33, 33A, 33B, 34, 34A
INVERTER CIRCUIT, 11, 21 FLAT DISPLAY APPARATUS, 12,
22 PIXEL SECTION, 13, 25 GLASS SUBSTRATE, 14A,
14B, 23A, 23B, 40A, 40B, 50A, 50B VERTICAL DRIVE
CIRCUIT, 15, 24 HORIZONTAL DRIVE CIRCUIT, 26
TIMING GENERATOR, 32 BUFFER CIRCUIT, TR1 - TR12
..... TRANSISTOR